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Serial No. 08/841,644

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INFORMATION DISCLOSURE STATEMENT

Applicants: Shunpei YAMAZAKI et al.

Filing Date: April 30, 1997

Group Art Unit: 2813

U.S. PATENT DOCUMENTS

Examiner Initial	Document Number	Date	Name	Class	Subclass	Filing Date (if appropriate)
CAI	3,775,262	11/27/1973	Heyerdahl			
CAI	3,890,636	06/17/1975	Harada et al.			
CAI	4,075,653	02/21/1978	Howard			
CAI	4,616,399	10/14/1986	Ooka			
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Examiner

Craig Johnson

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FOREIGN PATENT DOCUMENTS

Examiner Initial	Document Number	Date	Country	Class	Subclass	Translation	
						Yes	No
CAJ	0 072 216	04/12/1989	EP			Full Eng	
CAJ	01-123234	05/16/1989	JP			Eng Abst	
CAJ	01-274117	11/01/1989	JP			Eng Abst	
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CAJ	Wolf et al., "Silicon Processing for the VLSI Era", 1986, pp. 177-178, 198-207, 187-188, Vol. 1: Process Technology, Lattice Press
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CAJ	Ghandhi, "Silicon and Gallium Arsenide", 1983, pp. 401-411, VLSI Fabrication Principles

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